

## Features

- 100% avalanche tested
- Pb-free lead plating; RoHS compliant

## Application

- Synchronous Rectification
- PWM Application
- Power management
- Load Switch
- DC-DC converter

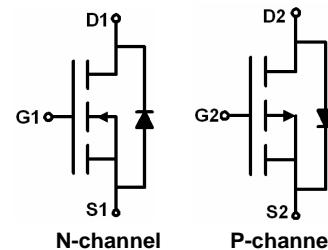
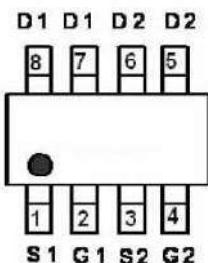
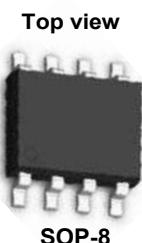
## Product Summary

### ● N-Channel

$V_{DS}$	40	V
$R_{DS(on),Typ}$	$V_{GS}=10V$	7.7 mΩ
$R_{DS(on),Typ}$	$V_{GS}=4.5V$	9.8 mΩ
$I_D$		10.9 A

### ● P-Channel

$V_{DS}$	-40	V
$R_{DS(on),Typ}$	$V_{GS}=-10V$	14.8 mΩ
$R_{DS(on),Typ}$	$V_{GS}=-4.5V$	18.4 mΩ
$I_D$		-7.6 A



## Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	$V_{DS}$	40	-40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	$\pm 20$	V
Continuous Drain Current $T_C=25^\circ\text{C}$	$I_D$	10.9	-7.6	A
Pulsed Drain Current (Note 1)	$I_{DM}$	43.6	-30.4	A
Avalanche Energy, Single Pulse	$E_{AS}$	10	15	mJ
Maximum Power Dissipation	$P_D$	2	2	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	-55 To 150	°C

## Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note2)	$R_{\theta JA}$	N-Ch	62.5	°C/W
		P-Ch	62.5	

Notes: EAS condition,  $V_{DS}=\pm 20V, V_{GS}=\pm 10V, L=0.5mH, R_g=25\Omega$

**N-CH Electrical Characteristics at Tj=25°C (unless otherwise specified) Static Characteristics**

Parameter	Symbol	Test Condition	Value			Unit
			Min.	Typ.	Max.	
Drain to Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250μA	1.0	1.5	2.5	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =40V, T <sub>j</sub> =25°C	-	0.01	1	μA
		V <sub>GS</sub> =0V, V <sub>DS</sub> =40V, T <sub>j</sub> =100°C		-	100	
Gate to Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	10	±100	nA
Drain to Source on Resistance	R <sub>DSS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	7.7	9.3	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A	-	9.8	12	
Gate Resistance	R <sub>G</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	-	1.8	-	Ω

**Dynamic Characteristics**

Parameter	Symbol	Test Condition	Value			Unit
			Min.	Typ.	Max.	
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =20V, f=1MHz	-	1980	-	pF
Output Capacitance	C <sub>oss</sub>		-	239	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	183	-	
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =20V, ID=30A	-	48	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	6.5	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	12	-	
Turn on delay time	t <sub>d(on)</sub>	V <sub>DS</sub> =20V, ID=30A, V <sub>GS</sub> =10V RG=1Ω, V <sub>GS</sub> =10V		7.5		ns
Rise time	t <sub>r</sub>			16		
Turn off delay time	t <sub>d(off)</sub>			22		
Fall time	t <sub>f</sub>			10		

**Diode Characteristics**

Parameter	Symbol	Test Condition	Value			Unit
			Min.	Typ.	Max.	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A	-	0.83	1.2	V

**P-CH Electrical Characteristics at  $T_j=25^\circ\text{C}$  (unless otherwise specified) Static Characteristics**

Parameter	Symbol	Test Condition	Value			Unit
			Min.	Typ.	Max.	
Drain to Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	-40	-		V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250\mu\text{A}$	-1.0	-1.8	-2.5	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-40\text{V}, T_j=25^\circ\text{C}$	-	-0.01	-1	$\mu\text{A}$
		$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-40\text{V}, T_j=100^\circ\text{C}$		-	-100	
Gate to Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	10	$\pm 100$	nA
Drain to Source on Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-20\text{A}$	-	14.8	19	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-15\text{A}$	-	18.4	23	
Gate Resistance	$R_{\text{G}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, f=1\text{MHz}$	-	6.5	-	$\Omega$

**Dynamic Characteristics**

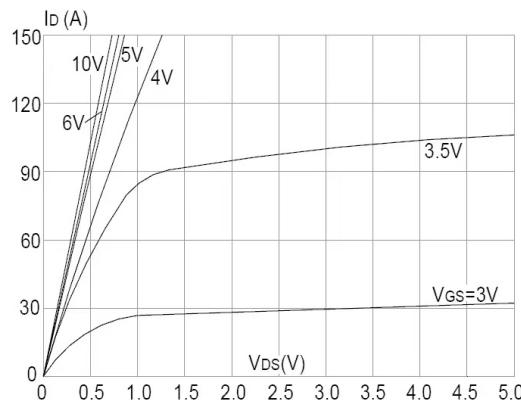
Parameter	Symbol	Test Condition	Value			Unit
			Min.	Typ.	Max.	
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-30\text{V}, f=1\text{MHz}$	-	1938	-	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		-	242	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	140	-	
Total Gate Charge	$Q_{\text{g}}$	$V_{\text{GS}}=-4.5\text{V}, V_{\text{DS}}=-20\text{V}, I_{\text{D}}=-30\text{A}$	-	58	-	$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$		-	7	-	
Gate-Drain Charge	$Q_{\text{gd}}$		-	10	-	
Turn on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}}=-10\text{V}, V_{\text{DD}}=-20\text{V}, I_{\text{D}}=-15\text{A}, \text{RGEN}=6.3$		10		$\text{ns}$
Rise time	$t_{\text{r}}$			24		
Turn off delay time	$t_{\text{d}(\text{off})}$			40		
Fall time	$t_{\text{f}}$			9		

**Diode Characteristics**

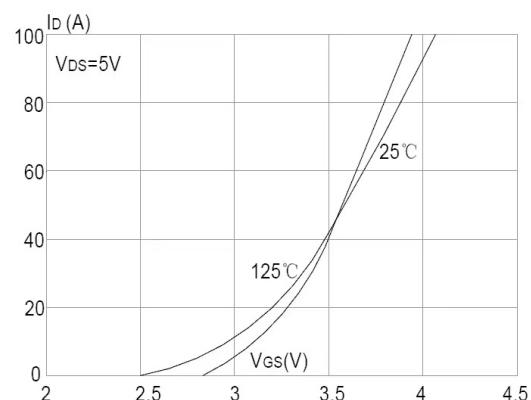
Parameter	Symbol	Test Condition	Value			Unit
			Min.	Typ.	Max.	
Diode Forward Voltage	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=-20\text{A}$	-	-0.92	-1.2	V

## N- Channel Typical Performance Characteristics

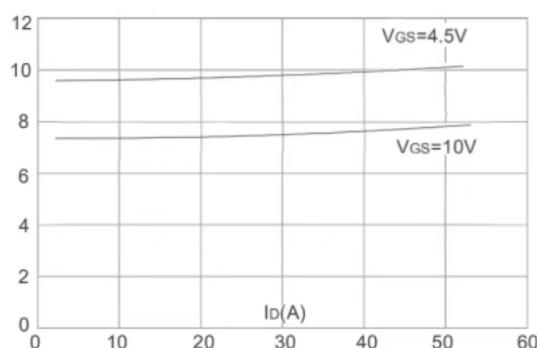
**Figure1:** Output Characteristics



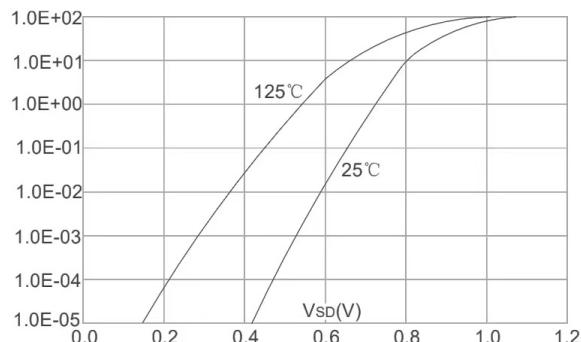
**Figure 2:** Typical Transfer Characteristics



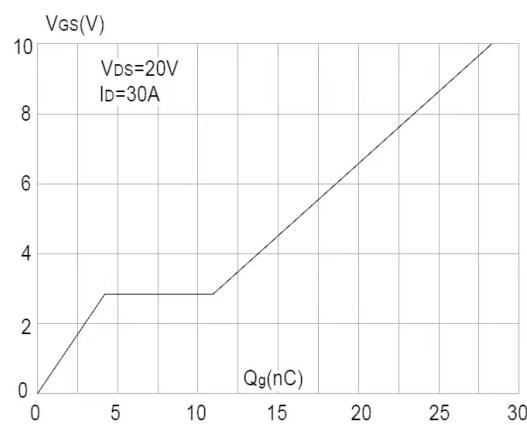
**Figure 3:** On-resistance vs. Drain Current



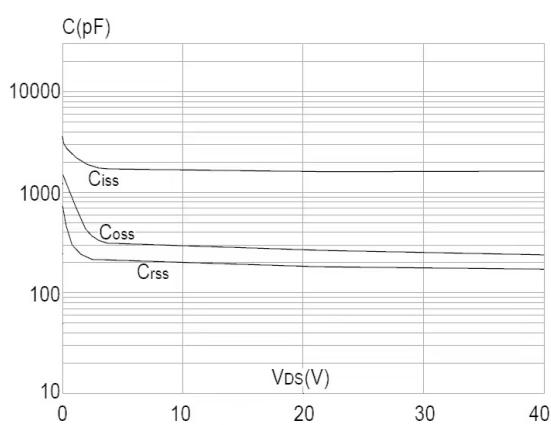
**Figure 4:** Body Diode Characteristics



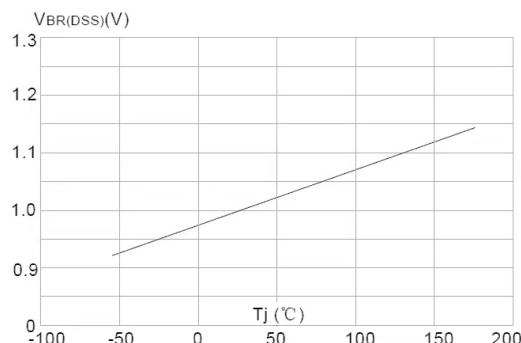
**Figure 5:** Gate Charge Characteristics



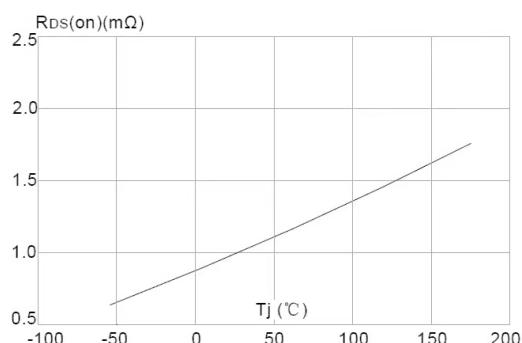
**Figure 6:** Capacitance Characteristics



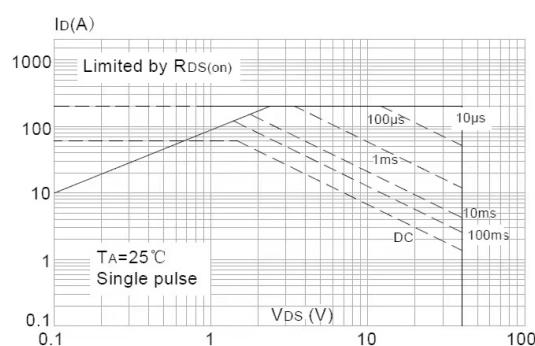
**Figure 7:** Normalized Breakdown Voltage  
vs. Junction Temperature



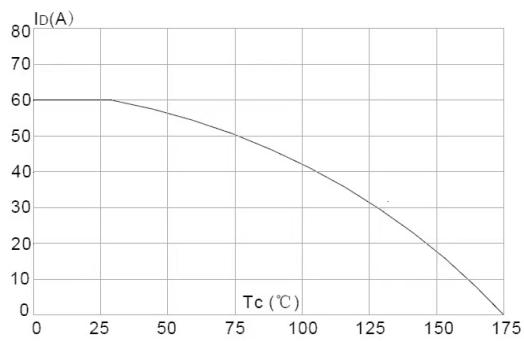
**Figure 8:** Normalized on Resistance  
vs. Junction Temperature



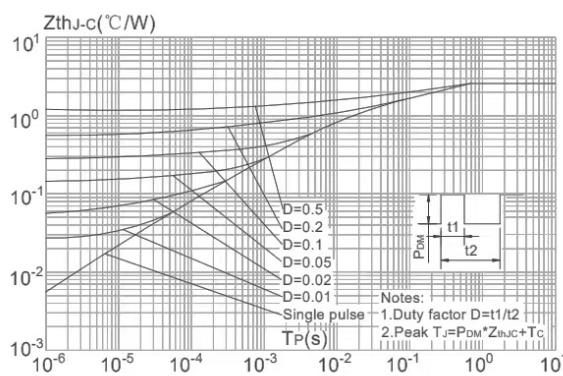
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current  
vs. Case Temperature

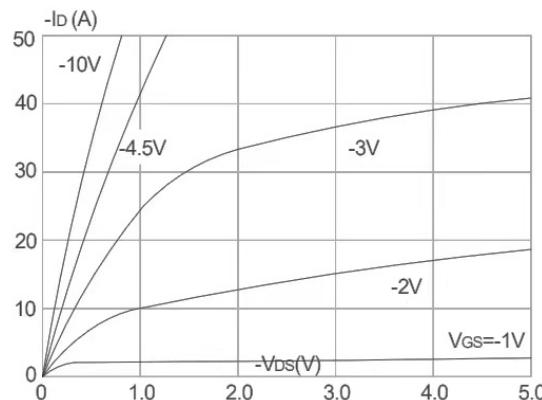


**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to- Case

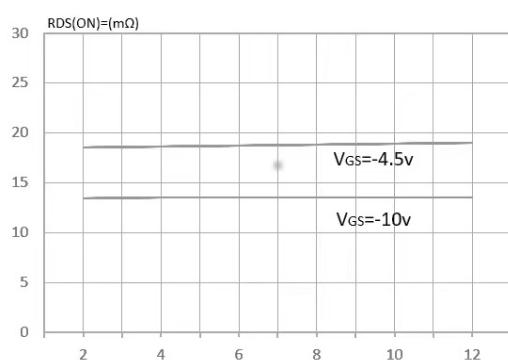


## P- Channel Typical Performance Characteristics

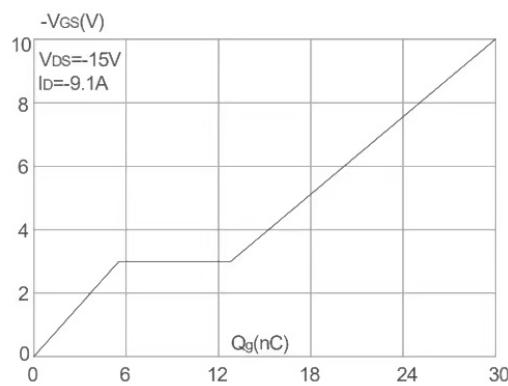
**Figure1:** Output Characteristics



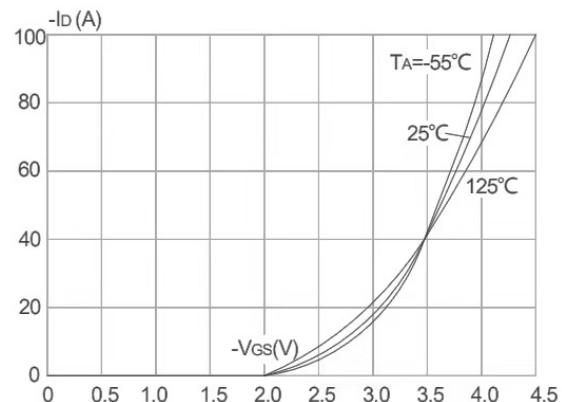
**Figure 3:** On-resistance vs. Drain Current



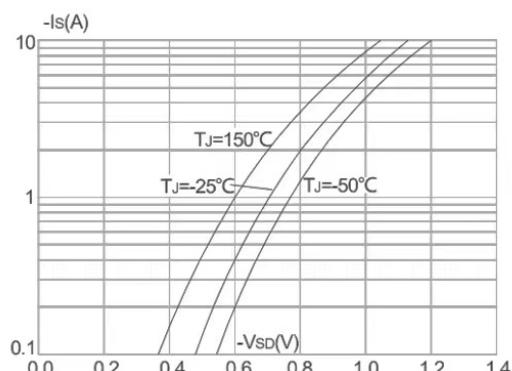
**Figure 5:** Gate Charge Characteristics



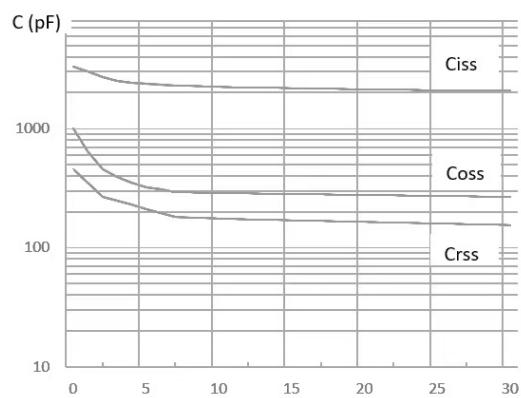
**Figure 2:** Typical Transfer Characteristics



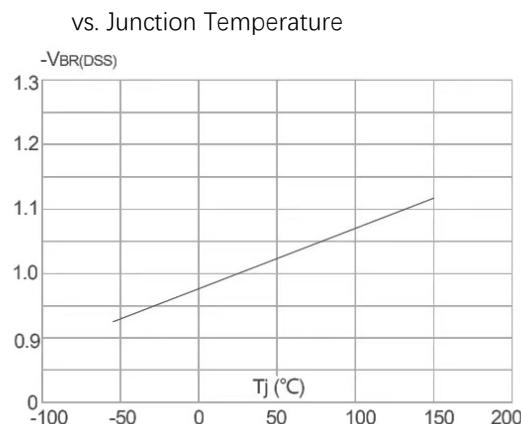
**Figure 4:** Body Diode Characteristics



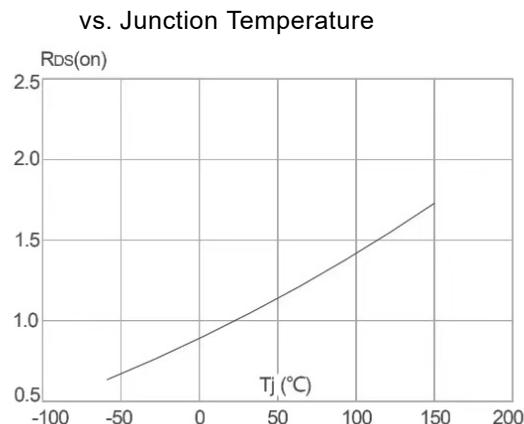
**Figure 6:** Capacitance Characteristics



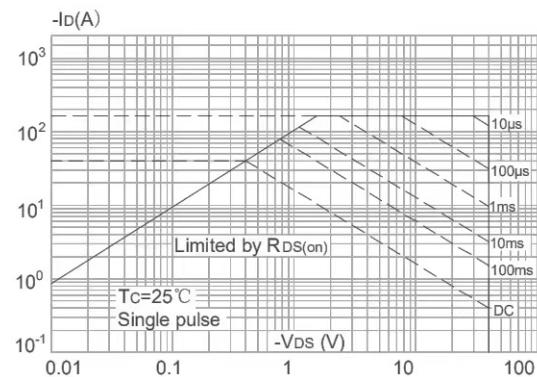
**Figure 7:** Normalized Breakdown Voltage



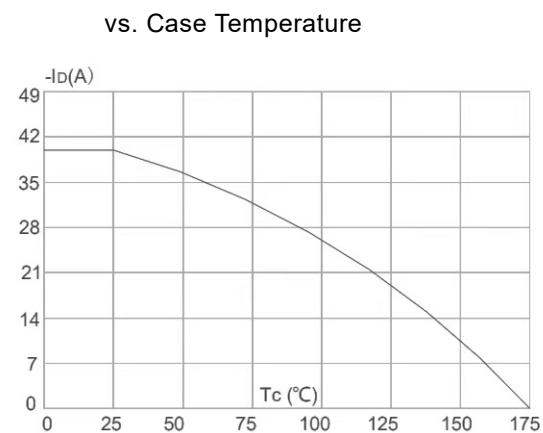
**Figure 8:** Normalized on Resistance



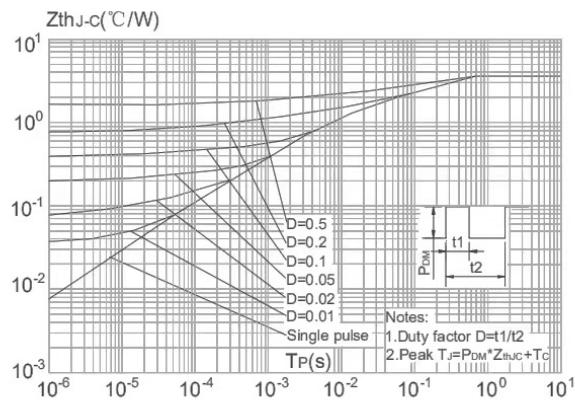
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current

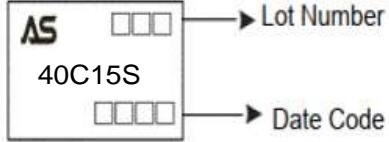


**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to- Case

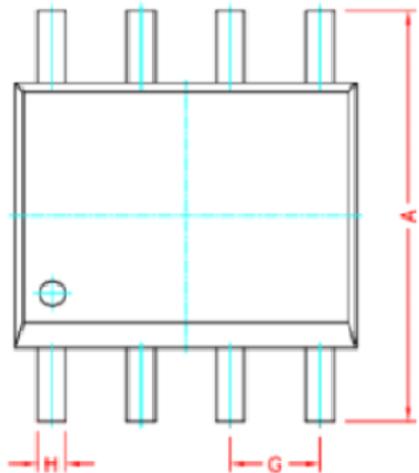


## Ordering and Marking Information

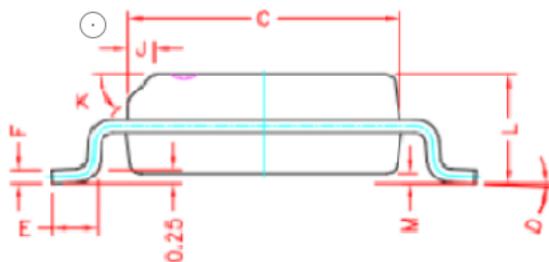
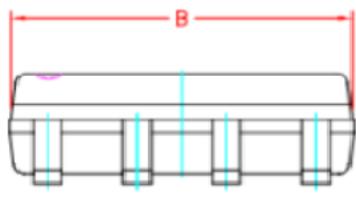
Ordering Device No.	Marking	Package	Packing	Quantity
ASDM40C15S-R	40C15S	SOP-8	Tape&Reel	4000/Reel

PACKAGE	MARKING
SOP-8	

# SOP-8



REF	DIMENSIONS	
	Millimeters	
	Min	Max
A	5.80	6.20
B	4.80	5.10
C	3.80	4.00
D	0°	8°
E	0.40	0.90
F	0.19	0.25
M	0.10	0.25
H	0.35	0.49
L	1.35	1.75
J	0.375	REF
K	45°	
G	1.27 TYP	



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